

Google

frame rate display adress partition "double buffering" OR "double buffer" OR

Search

Web Books Results 1 - 10 of about 31,600 for frame rate display adress partition "double buffering" OR "

Did you mean: frame rate display *address portion* "double buffering" OR "double buffer" OR "double buffered"

Method for reducing transport delay in a synchronous image ...

(c) rendering the primitives into a double buffered pixel frame buffer by using ... Address generator345/84, Light-controlling display elements345/522, ...

www.patentform.us/patents/6801205/claims.html - 23k - Cached - Similar pages

Method for reducing transport delay in a synchronous image ...

A method for enabling reduced transport display in a computer image generator ... transformations and rendering and in less than one display frame. ...

www.patentform.us/patents/6801205.html - 18k - Cached - Similar pages

More results from www.patentform.us »

dV/WebFly Developer Guide

Toggles double-buffering. By default rendering is double buffered (so that the next frame can be ... You use this to increase the display frame rate. ...

onyx2ced.frascati.enea.it/grafica/Corsi/Divisioni/UNIXMAN/DVISEUSA/WEBDEV/using.htm -

14k - Cached - Similar pages

Storage and retrieval of data using residual frame buffer memory ...

A double buffered frame buffer system doubles the number of the planes available for storing clipping data. For example, a double buffer version of frame ...

www.freepatentsonline.com/EP0644521.html - 34k - Cached - Similar pages

Computer Graphics: Principles and Practice - Google Books Result

by James D. Foley, Andries Van Dam, Steven K. ... - 1996 - Computers - 1175 pages

This requirement precludes double-buffering: scan converting a new image into ... display processor discussed in the previous section by making the frame ...

books.google.com/books?isbn=0201848406...

Real Time H.263 Video Codec Using Parallel Dsp - Image Processing ...

the same image size, frame rate and other conditions. ... frame-capture unit and a VGA.

display unit is used for the work. 2.2 Partitioning the H.263 on MVP ...

ieeexplore.ieee.org/iel5/4998/13789/00638839.pdf?arnumber=638839 - Similar pages

(WO/1995/013601) PARTITIONED DISPLAY APPARATUS

Such double buffering and also the VRAM address generation and refresh However, partitioning a display in accordance with the present invention is ...

www.wipo.int/pctdb/en/wo.jsp?IA=WO1995013601&DISPLAY=DESC - 49k -

Cached - Similar pages

[PDF] Lightning-2: A High-Performance Display Subsystem for PC Clusters

File Format: PDF/Adobe Acrobat - View as HTML

The pixel data must be double-buffered to allow for the simultaneous display of one frame of data while receiving the next frame of data. To address the ...

graphics.stanford.edu/papers/lightning2/lightning2_print.pdf - Similar pages

HP CCN - Scalable Interactive Ray-Casting of Volumes white paper

We conclude by discussing technical and fundamental issues to address in future This makes no change in the effective frame rate, because the full SBP ...
www.hp.com/techservers/hpccr/sci_vis/f1_pvr3_abt.html · 76k · [Cached](#) · [Similar pages](#)

OpenGL Performance Optimization

For smooth animation one must maintain a high, constant frame rate. Double buffering has an important effect on this. Suppose your application needs to ...
www.mesa3d.org/brianp/sig97/perfop1.htm · 37k · [Cached](#) · [Similar pages](#)

Did you mean to search for: frame rate display *address portion* "double buffering" OR "double buffer" OR "double buffered"

1 2 3 4 5 6 7 8 9 10 [Next](#)

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#) | [Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)